Protection and Control of Systems with Converter Interfaced Generation

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Challenges Generated by Renewables

Variability, Impact on Reliability

Creation of High Ramp Rates

CIG: Protection, Stability and Control

Low Voltage Ride Through

Protection

Stability – traditional stability is meaningless
Protection Challenges

Protection of CIG (Converter Interfaced Generation) and associated circuits and components is challenging due to:

(a) Insufficient separation between fault and load currents caused by converter interfaced sources,

(b) Large fault contributions from utility side – small fault contributions from inverter based sources and

(c) Requirement to operate in utility connected mode as well as islanded mode.

(d) Lack of inertia

These issues are common for wind systems, PV systems and μGrids.
Another Operational Issue:

Deployment of Converter Interfaced Generation (CIG) is ever increasing (wind, PV, etc.) with many systems reaching more than 50% presently. Stabilization and riding through disturbances is a problem of increasing significance.

Need for CIG to smoothly follow the oscillations of the system and avoid excessive transients and activation of transient ride through controls, i.e. provides stabilization control to the CIG.

**Enabling Technology:** Dynamic State Estimation based protection which provides reliable protection and estimation of frequency and rate of frequency change locally at the inverter as well as at the system using only local measurements. This information enables supplementary inverter control for smooth inverter/system synchronization and disturbance ride through.
Example Systems

WTS

575 V / 34.5 kV

\[ \text{URD} \]

34.5 / 230 kV

13.8 / 230 kV

μGrid

480 V / 13.8 kV

\[ F_1 \]

\[ F_2 \]
Dynamic State Estimation Based Protection

- Setting-less protective relay
- Sampled Value based dynamic state estimation
- Fast fault detection (sub ms)
- Measurement of frequency
- Measurement of ROCOF

Component Model
- Algebraic
- Quadratic
- Companion Model

Data Acquisition
4,000 s/s to 20,000 s/s

Measurements
- Actual
- Derived
- Virtual
- Pseudo

Dynamic State Estimation
Minimize
\[ J = \sum (z - h(x))^2 \]

Protection Logic
Chi-Square Test

Trip
No Trip
Example of Fault Detection: Comparison of Legacy Protection and DSE Based Protection

Table 1: Example μGrid circuit parameters

<table>
<thead>
<tr>
<th>Object</th>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>System</td>
<td>Line to line voltage</td>
<td>480 $V$</td>
</tr>
<tr>
<td></td>
<td>Fundamental frequency</td>
<td>60 $Hz$</td>
</tr>
<tr>
<td></td>
<td>Length of the monitored circuit</td>
<td>375 feet</td>
</tr>
<tr>
<td>Monitored Circuit</td>
<td>Positive (Negative) sequence</td>
<td>0.0957+ $j$ 0.0153 $\Omega$</td>
</tr>
<tr>
<td></td>
<td>Zero sequence</td>
<td>0.2186+ $j$ 0.1555 $\Omega$</td>
</tr>
</tbody>
</table>
Legacy Protection: Distance Relay

Performance of Legacy Protection Function

- Incorrect non-operation (mis-operation)
- Inherent time delays due to phasor based detection circuit
Performance of Setting-Less Protective Relay

- Correctly detects the fault.
- Detection is achieved in less than 0.2 ms
Operational Stability:

Need for CIG to smoothly follow the oscillations of the system and avoid excessive transients and activation of transient ride through controls, i.e. provides stabilization control to the CIG.

**Enabling Technology:** The setting-less relay provides estimated frequency and rate of frequency change locally at the inverter as well as at the system using only local measurements. This information enables supplementary inverter control for smooth inverter/system synchronization and disturbance ride through.
Example Test System

- Wind Turbine System (WTS), with 50 Hz voltage input
- Oscillating source with frequency $60 \pm 0.1$ Hz
- 34.5kV Transmission line,
- Measurements at the Local side only
- Results have been obtained for different lengths of line

**Measurements**: Three-phase local measurements

**Target**: Estimate three-phase local & remote measurements, $f$ and $df/dt$

**Method**: Dynamic State Estimation

**Performance**: how accurate $f$ and $df/dt$
Case 1: 1.5 miles – Side 1

- Actual Voltage Phase A Side 1 (V)
- Actual Voltage Phase B Side 1 (V)
- Actual Voltage Phase C Side 1 (V)
- DSE Output Voltage Phase A Side 1 (V)
- DSE Output Voltage Phase B Side 1 (V)
- DSE Output Voltage Phase C Side 1 (V)
- Actual Frequency Side 1 (Hz)
- Estimated Frequency Side 1 (Hz)
- Frequency Error Side 1 (Hz)
- Actual dF/dt Side 1 (Hz/s)
- Estimated dF/dt Side 1 (Hz/s)
- Error dF/dt Side 1 (Hz/s)
Case 1: 1.5 miles – Side2

- Actual Voltage Phase A, B, C (V)
- DSE Output Voltage Phase A, B, C (V)
- Actual Frequency (Hz)
- Estimated Frequency (Hz)
- Frequency Error (Hz)
- Actual df/dt (Hz/s)
- Estimated df/dt (Hz/s)
- Error df/dt (Hz/s)
Case 2: 2.5 miles – Side 1

Actual Voltage PhaseA Side1 (V)
Actual Voltage PhaseB Side1 (V)
Actual Voltage PhaseC Side1 (V)
DSEOutput Voltage PhaseA Side1 (V)
DSEOutput Voltage PhaseB Side1 (V)
DSEOutput Voltage PhaseC Side1 (V)

Actual Freq Side1 (Hz)
Estimated Freq Side1 (Hz)

Freq Error Side1 (Hz)

Actual dfdt Side1 (Hz/s)
Estimated dfdt Side1 (Hz/s)

Error dfdt Side1 (Hz/s)
Case 2: 2.5 miles – Side2

Actual Voltage Phase A Side 2 (V)
Actual Voltage Phase B Side 2 (V)
Actual Voltage Phase C Side 2 (V)

DSE Output Voltage Phase A Side 2 (V)
DSE Output Voltage Phase B Side 2 (V)
DSE Output Voltage Phase C Side 2 (V)

Actual Freq Side 2 (Hz)
Estimated Freq Side 2 (Hz)

Freq Error Side 2 (Hz)
Actual dfdt Side 2 (Hz/s)
Estimated dfdt Side 2 (Hz/s)

Error dfdt Side 2 (Hz/s)
Case 3: 4 miles – Side 1

- Actual_Voltage_PhaseA_Side1 (V)
- Actual_Voltage_PhaseB_Side1 (V)
- Actual_Voltage_PhaseC_Side1 (V)

- DSEOutput_Voltage_PhaseA_Side1 (V)
- DSEOutput_Voltage_PhaseB_Side1 (V)
- DSEOutput_Voltage_PhaseC_Side1 (V)

- Actual_Freq_Side1 (Hz)
- Estimated_Freq_Side1 (Hz)

- Freq_Error_Side1 (Hz)

- Actual_dfdt_Side1 (Hz/s)
- Estimated_dfdt_Side1 (Hz/s)

- Error_dfdt_Side1 (Hz/s)
Case 3: 4 miles – Side2

Actual Voltage Phase A Side2 (V)
Actual Voltage Phase B Side2 (V)
Actual Voltage Phase C Side2 (V)

DSEOutput Voltage Phase A Side2 (V)
DSEOutput Voltage Phase B Side2 (V)
DSEOutput Voltage Phase C Side2 (V)

Actual Freq Side2 (Hz)
Estimated Freq Side2 (Hz)

Freq Error Side2 (Hz)

Actual dfdt Side2 (Hz/s)
Estimated dfdt Side2 (Hz/s)

Error dfdt Side2 (Hz/s)
# Summary of Results – $f$ and $df/dt$

Frequency: 59.98Hz $\sim$ 60.09Hz, Rate of Change: -0.6 $\sim$ 0.6Hz/s

## Side 1 Results

<table>
<thead>
<tr>
<th>Case number</th>
<th>Line length</th>
<th>Frequency error</th>
<th>dFreq/dt error</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>1.5 miles</td>
<td>$-1.887 \times 10^{-5}$ $\sim$ $1.827 \times 10^{-5}$ Hz</td>
<td>$-1.24 \times 10^{-4}$ $\sim$ $-2.602 \times 10^{-6}$ Hz/s</td>
</tr>
<tr>
<td>2</td>
<td>2.5 miles</td>
<td>$-2.005 \times 10^{-5}$ $\sim$ $1.807 \times 10^{-5}$ Hz</td>
<td>$-1.28 \times 10^{-4}$ $\sim$ $9.340 \times 10^{-6}$ Hz/s</td>
</tr>
<tr>
<td>3</td>
<td>4 miles</td>
<td>$-1.853 \times 10^{-5}$ $\sim$ $1.803 \times 10^{-5}$ Hz</td>
<td>$-1.35 \times 10^{-4}$ $\sim$ $4.507 \times 10^{-6}$ Hz/s</td>
</tr>
</tbody>
</table>

## Side 2 Results

<table>
<thead>
<tr>
<th>Case number</th>
<th>Line length</th>
<th>Frequency error</th>
<th>dFreq/dt error</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>1.5 miles</td>
<td>$-6.513 \times 10^{-5}$ $\sim$ $1.77 \times 10^{-4}$ Hz</td>
<td>$-1.144 \times 10^{-3}$ $\sim$ $1.049 \times 10^{-3}$ Hz/s</td>
</tr>
<tr>
<td>2</td>
<td>2.5 miles</td>
<td>$-9.464 \times 10^{-5}$ $\sim$ $2.82 \times 10^{-4}$ Hz</td>
<td>$-1.906 \times 10^{-3}$ $\sim$ $1.689 \times 10^{-3}$ Hz/s</td>
</tr>
<tr>
<td>3</td>
<td>4 miles</td>
<td>$-1.29 \times 10^{-4}$ $\sim$ $4.51 \times 10^{-4}$ Hz</td>
<td>$-2.896 \times 10^{-3}$ $\sim$ $2.751 \times 10^{-3}$ Hz/s</td>
</tr>
</tbody>
</table>
CIG Performance with Supplementary Controls
Predictive Inverter Control Enabled by Dynamic State Estimator
The frequency-modulation control *gradually* modulates the frequency of converter switching sequences to slowly synchronize with remote-end transmission and prevent large current transient. The modulation-index and phase-angle modulation controls modulate the amplitude and phase angle of the AC output voltage by controlling the duty ratio and start time of switching sequences.
Frequency- and Phase Angle-Modulation Controls (P-Q Mode)

Switching-Sequence Definition

**Negative Edge:**

\[
ct_{(2i)a} = \frac{\theta_{cntrl}}{2\pi f_{cntrl}} + \frac{i}{f_S} + f_N \left( \frac{i}{f_S} + \frac{\theta_{cntrl}}{2\pi f_{cntrl}}, m_{cntrl} \right), \quad i = 0, 1, 2, ..., 10
\]

\[f_N (a, b, c) = \text{solve} \left( c \cdot \cos \left( 2\pi f_{cntrl} \cdot t \right) = 2 \cdot a \cdot \left( t - b \right), t \right), \quad i = 0, 1, 2, ..., 10
\]

**Positive Edge:**

\[
ct_{(2i+1)a} = \frac{\theta_{cntrl}}{2\pi f_{cntrl}} + \frac{i}{f_S} + f_P \left( \frac{i}{f_S} + \frac{\theta_{cntrl}}{2\pi f_{cntrl}}, m_{cntrl} \right), \quad i = 0, 1, 2, ..., 9
\]

\[f_P (a, b, c) = \text{solve} \left( c \cdot \cos \left( 2\pi f_{cntrl} \cdot t \right) = -2 \cdot a \cdot \left( t - \frac{1}{a} \right), t \right), \quad i = 0, 1, 2, ..., 9
\]

where:
- \( f_S \): Switching frequency (Hz).
- \( f_N (a, b, c) \): A function for calculating the negative-edge time per each sampling period (sec).
- \( f_P (a, b, c) \): A function for calculating the positive-edge time per each sampling period (sec).
Simulation Results:
CIG System without Predictive Inverter Control

We perform this simulation test for a case that a remote converter-interfaced generation (CIG) is under frequency oscillation when a local CIG does not have the Predictive Inverter Controller. The frequencies of two CIGs cannot synchronize with each other, consequently power-factor angle difference oscillates. Thus the CIG systems exceed their desirable operation constraints. Therefore, CIG system requires the Predictive Inverter Control to minimize the transient.
Simulation Results:
CIG System with Predictive Inverter Control

We perform this simulation test for a case that a remote converter-interfaced generation (CIG) is under frequency oscillation when a local CIG has the Predictive Inverter Controller (P-Q mode: Controlled at 2 MW and 0.5 Mvar). The frequencies of two CIGs can synchronize with each other and power-factor angle difference is kept within their desirable operation constraints. Therefore, CIG system with the Predictive Inverter Controller can successively minimize the transient.
Conclusions

• As the penetration level of CIG increases, one observes a number of issues in the normal operation of the system, oscillations among inverters, reduced fault currents and legacy protection systems failures.

• The setting-less protective relay provides reliable protection for system with converter interfaced generation.

• The setting-less relay (dynamic state estimation) also provides the enabling technology for supplementary sluggish control to limit the oscillations among inverters and synchronize inverters with the power system and minimize activation of LVRT logic.

• The accuracy of the proposed methods to provide the necessary signals has been demonstrated.
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